



DDL4848-48

48V bidirectional DC/DC Converter

Description

The DDL4848-48 is a non-isolated, low voltage, high-power DC/DC Converter handling energy transfer between two ports (Port A and Port B) in either direction. During power transfer from Port A to Port B, the converter operates in buck mode and provides a reduced voltage level at Port B. In the reverse direction, the converter works in boost mode and increases the voltage level on Port A.

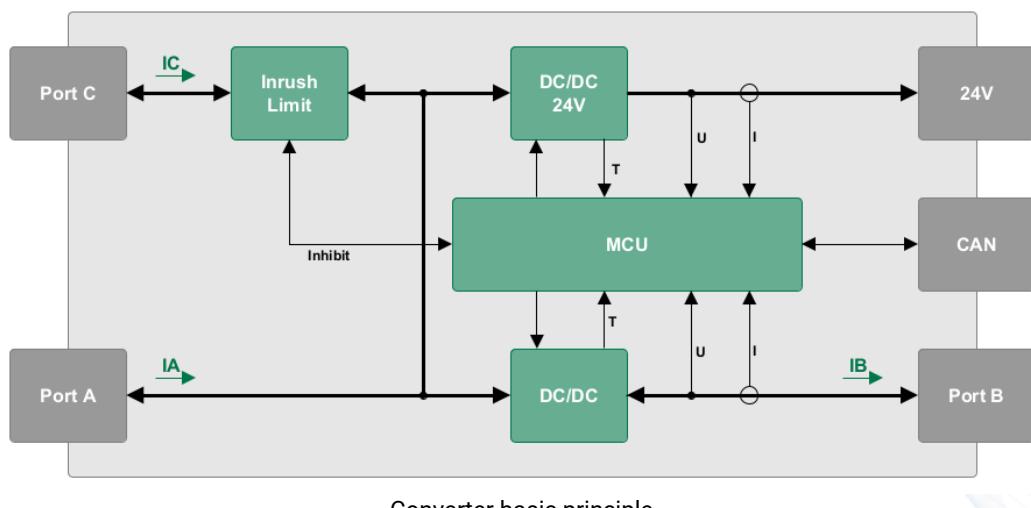
The dedicated input Port C in parallel to Port A is equipped with a circuitry limiting the inrush current. Therefor a connected power supply is prevented from high current load during startup.

An additional +24V constant voltage output features a power supply for a lot of applications. With the CAN interface, a variety of parameters can be set individually. Several safety functions e.g., overvoltage, overcurrent and overtemperature protection are integrated.



- Energy recovery (Recuperation)
- Programmable input/output
- High efficiency
- Remote control (CAN)
- Overload protection
- Low standby power consumption
- Port A input current up to 100A
- Port B input current up to -85A
- Inrush current limitation (Port C)
- Auxiliary 24V output

Technical Data Sheet





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Specification

The following parameters are valid for operation at 25°C and under nominal conditions, unless specifically stated otherwise. Nominal condition includes in particular $U_C > U_B$, $U_A > U_B$ and $U_A > 20V$.

General

| Parameter | Symbol | Value | | | Unit | Comment |
|---|----------------|-------|------|-----------------|------|---|
| | | min. | typ. | max. | | |
| Overvoltage tol. Port A Port B Port B Sense Port C 24V | $U_{A,pk}$ | 60 | - | - | V | 10s, No protection against reverse current |
| | $U_{B,pk}$ | 60 | - | - | V | |
| | $U_{Bsns,pk}$ | 60 | - | - | V | |
| | $U_{C,pk}$ | 60 | - | - | V | |
| | $U_{24V,pk}$ | 27 | - | - | V | |
| | | | | | | |
| Inrush Current Port A Port B | $I_{A,inrush}$ | - | - | $I_{A,nom,max}$ | A | not actively limited |
| | $I_{B,inrush}$ | - | - | $I_{B,nom,max}$ | A | not actively limited |
| Dropout | $U_{A,B,Drop}$ | - | - | 2 | V | $U_{A,C} - U_B$ at $I_{B,nom,max}$ |
| Efficiency Port A to Port B Port B to Port A 24V | $\eta_{A,B}$ | 96 | 97 | - | % | for $I_B > 0.5 I_{B,nom,max}$ for $I_{A,C} < 0.5 I_{A,nom,min}$ for $P_{24V} > 0.3 P_{24V,nom}$ |
| | $\eta_{B,A}$ | 94 | 95 | - | % | |
| | η_{24V} | 95 | 97 | - | % | |
| | | | | | | |
| Withstand Voltage Ports A,B,C to Case | $U_{Iso,wth}$ | 100 | - | - | V | |
| Impedance Ports A,B,C to Case | Z_{Iso} | - | 28 | | uF | Depending on EMC |
| Startup time ⁽¹⁾ | t_{Setup} | - | - | 1.5 | s | |

(1) Startup time is defined as the timespan between $U_{A,nom,max} > U_A > U_{A,nom,min}$ and start of operation of Ports A,B and 24V outputs.

Port A

| Parameter | Symbol | Value | | | Unit | Comment |
|---|----------------|-------|------|-------|------|---|
| | | min. | typ. | max. | | |
| Voltage Ripple&Noise Load Regulation Line Regulation | $U_{A,nom}$ | 20 | - | 55 | V | pk-pk, 20MHz, 47µF |
| | $U_{A,Ripple}$ | - | - | 500 | mV | |
| | $dU_{A,load}$ | -1.0 | - | +1.0 | V | |
| | $dU_{A,line}$ | 0.25 | - | +0.25 | V | |
| Current | $I_{A,nom}$ | -33 | - | 100 | A | |
| Load transient Deviation Recovery | $d_{A,trans}$ | -10 | - | - | % | Load Jump 80/20% Relative to $U_{A,Set}$ |
| | $t_{A,trans}$ | - | - | +10 | ms | |
| | | | | 200 | | |



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Port B

| Parameter | Symbol | Value | | | Unit | Comment |
|-------------------------------|-----------------------|-------|------|------|------|---|
| | | min. | typ. | max. | | |
| Start Voltage | $U_{B,\text{Start}}$ | 14 | - | - | V | MCU Wakeup Voltage without previous operation |
| Voltage Input | $U_{B,\text{nom}}$ | 0 | - | 55 | V | |
| Derated Input. ⁽¹⁾ | $U_{B,\text{in,nom}}$ | 22 | - | 55 | V | Fully operational |
| Ripple&Noise | $U_{B,\text{in,min}}$ | - | 2 | - | V | While $U_A > U_{A,\text{nom,min}}$ |
| | $U_{B,\text{Ripple}}$ | - | - | 200 | mV | pk-pk, 20MHz, 47µF |
| Current | $I_{B,\text{nom}}$ | -85 | - | 100 | A | |

(1) Available Power is limited by $U_B * I_{B,\text{nom,min}}$

Port C

| Parameter | Symbol | Value | | | Unit | Comment |
|-----------------------------------|--------------------|-------|------|------|------|--|
| | | min. | typ. | max. | | |
| Voltage | $U_{C,\text{nom}}$ | 20 | - | 55 | V | Fully operational |
| Current | $I_{C,\text{nom}}$ | -33 | - | 100 | A | |
| Inrush Limiter Deactivation Volt. | U_{IL} | - | 4 | - | V | Voltage differential for inrush current limitation |
| Resistance | R_{IL} | - | 50 | - | Ω | |
| End Delay | t_{IL} | - | 1 | - | s | Resistor is shorted at inrush current limitation end |

24V Output

| Parameter | Symbol | Value | | | Unit | Comment |
|--------------------------|-------------------------|-------|------|-------|------|------------------------|
| | | min. | typ. | max. | | |
| Voltage Tolerance | $U_{24V,\text{nom}}$ | - | 24 | - | V | Fixed |
| Ripple&Noise | $dU_{24V,\text{all}}$ | -0.72 | - | +0.72 | V | Line + Load + Setpoint |
| Rise time ⁽¹⁾ | $U_{24V,\text{Ripple}}$ | - | - | 200 | mV | pk-pk, 20MHz, 47µF |
| | $t_{24V,\text{rise}}$ | - | - | 100 | ms | |
| Current Limit | $I_{24V,\text{cont}}$ | 6.5 | - | - | A | |
| | $I_{24V,\text{lim}}$ | - | - | 8 | A | |
| Power | $P_{24V,\text{nom}}$ | 150 | - | - | W | |

(1) Rise time is defined from the point of time where $U_A \geq U_{A,\text{nom}}$ is applied for $t > t_{\text{Setup}}$ and the point of time where $U_{24V} = U_{24V,\text{nom}} \pm dU_{24V,\text{all}}$.



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Port A Control

| Parameter | Symbol | Value | | | Unit | Comment |
|------------------|------------------------|-------|------|------|--------|------------------|
| | | min. | typ. | max. | | |
| Voltage Setpoint | $U_{A,\text{Set}}$ | 20 | - | 53 | V | CAN programmable |
| Tolerance | $dU_{A,\text{Set}}$ | -1.0 | - | +1.0 | V | |
| Resolution | $S_{A,\text{Set,nom}}$ | - | 10 | - | mV/Bit | |

Port B Control

| Parameter | Symbol | Value | | | Unit | Comment |
|--|--|-------|------|------|--------|------------------|
| | | min. | typ. | max. | | |
| Input Current Setpoint | $I_{B,\text{In},\text{Set}}$ | -15 | - | -85 | A | CAN programmable |
| Tolerance | $dI_{LV,\text{In},\text{Set}}$ | -5 | - | +5 | A | |
| Resolution | $S_{ILV,\text{In},\text{Set,nom}}$ | - | 10 | - | mA/Bit | |
| Input Power Resolution | $P_{\text{In},\text{Set}}$ | -3000 | - | 300 | W | CAN programmable |
| $S_{P,\text{In},\text{nom}}$ | - | 1 | - | | W/Bit | |
| Output Voltage Setpoint | $U_{B,\text{Out},\text{Set}}$ | 6 | - | 50 | V | CAN programmable |
| Tolerance | $dU_{B,\text{Out},\text{Set}}$ | -0.5 | - | +0.5 | V | |
| Resolution | $S_{UB,\text{Out},\text{Set,nom}}$ | - | 10 | - | mV/Bit | |
| Output Current Setpoint | $I_{B,\text{Out},\text{Set}}$ | 15 | - | 100 | A | CAN programmable |
| Tolerance | $dI_{LV,\text{Out},\text{Set}}$ | -5 | - | +5 | A | |
| Resolution | $S_{ILV,\text{Out},\text{Set,nom}}$ | - | 10 | - | mA/Bit | |
| Output Power Resolution | $P_{\text{Out},\text{Set}}$ | 500 | - | 5000 | W | CAN programmable |
| $S_{P,\text{Out},\text{nom}}$ | - | 1 | - | | W/Bit | |
| Output Delay Resolution | $t_{\text{Out},\text{del},\text{Set}}$ | 100 | - | 5000 | ms | CAN programmable |
| $S_{t,\text{Out},\text{del},\text{nom}}$ | - | 10 | - | | ms/Bit | |

Port C Control

| Parameter | Symbol | Value | | | Unit | Comment |
|----------------------------------|--------------------------------|-------|------|------|--------|------------------|
| | | min. | typ. | max. | | |
| Port C to A threshold Resolution | $U_{CA,\text{thr},\text{Set}}$ | 900 | - | 5000 | mV | CAN programmable |
| $S_{UCA,\text{thr},\text{nom}}$ | - | 10 | - | - | mV/Bit | |



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Monitoring

| Parameter | Symbol | Value | | | Unit | Comment |
|---|--|----------------|--------------|----------------|------------------|--|
| | | min. | typ. | max. | | |
| Sense Resolution | n_{sns} | - | 12 | - | Bit | |
| Sense Bandwidth | f_{sns} | 50 | - | - | Hz | |
| Voltage Sense Tolerance Slope | dU_{sns} $S_{Usns,nom}$ | -0.5 - | - 10 | +0.5 - | V mV/Bit | |
| Current Sense Port A tolerance Port B tolerance Slope | $dI_{A,sns}$ $dI_{B,sns}$ $S_{Isns,nom}$ | -10 -5 - | - - 10 | +10 +5 - | A A mA/Bit | for $I_B > 0.2 I_{B,nom,max}$ for $I_B > 0.2 I_{B,nom,max}$ |
| Temperature Sense Tolerance Slope | dT_{sns} $S_{Tsns,nom}$ | -5 - | - 1 | +5 - | °C °C/Bit | |

Environmental Conditions

The DC/DC-converter is designed to be operated under following environmental conditions.

| Parameter | Symbol | Value | | | Unit | Comment |
|--------------------------|-----------------|-------|------|------|------|--|
| | | min. | typ. | max. | | |
| Storage Temperature | T_{Stor} | -25 | - | 60 | °C | |
| Ambient Temperature | $T_{amb,nom}$ | 0 | - | 80 | °C | |
| Baseplate Temperature | $T_{base,nom}$ | 0 | - | 55 | °C | |
| Thermal Protection Limit | $T_{Base,Prot}$ | 60 | - | - | °C | Converter will be deactivated above 60°C |
| Humidity | Φ_{Nom} | 20 | - | 95 | % | Non-condensing |
| Airflow | v_{Air} | 0 | - | - | m/s | No Airflow |

Certification

| Parameter | Standard | Comment |
|-----------|-------------|---|
| Safety | EN62368-1 | |
| Emission | EN61000-6-4 | |
| Immunity | EN61000-6-2 | Basic standards: EN61000-4-2, EN61000-4-3, EN61000-4-4, EN61000-4-5, EN61000-4-6, EN61000-4-8, EN61000-4-11 |



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Notice:

The product information and specifications may be subject to changes even without prior written notice. The product has been designed for various applications; its suitability lies in the responsibility of each customer. The products are not authorized for use in safety-critical applications without Querom's explicit written consent. A safety-critical application is an application where a failure may reasonably be expected to endanger or cause loss of life, inflict bodily harm or damage property. The applicant shall indemnify and hold harmless Querom, its affiliated companies and its representatives against any damage claims in connection with the unauthorized use of Querom products in such safety-critical applications.

Document history

| Version | Date | Author | Reason for change |
|---------|------------|--------|-------------------|
| V1.0 | 17.04.2024 | JS | Initial |
| | | | |
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Querom

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